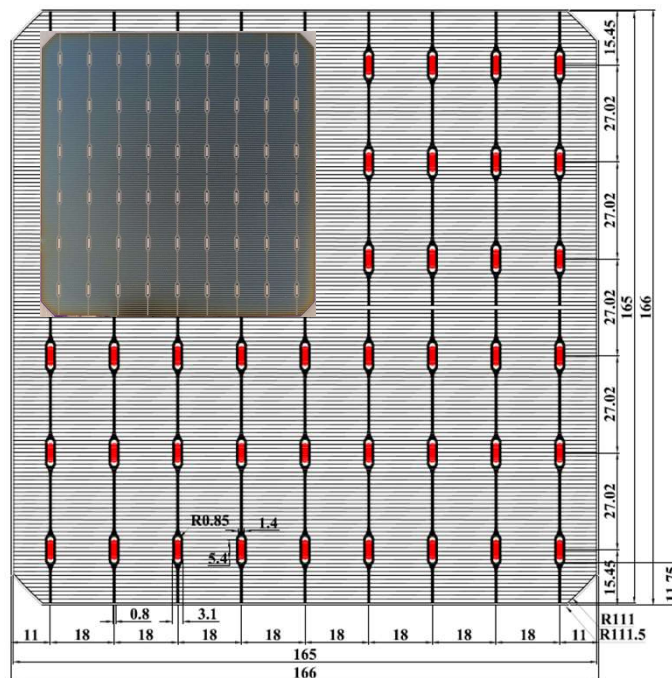
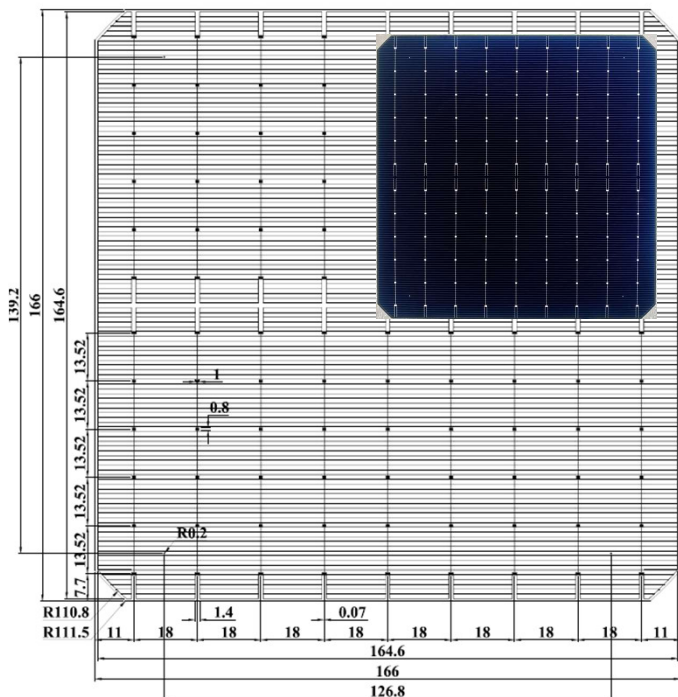


1 FRONT AND BACK OF THE CELL AND REFERENCE DIMENSIONS



2 ELECTRICAL DATA

Efficiency(%)	Pmpp(W)	Voc(V)	Isc(A)	Vmpp(V)	Impp(A)
22.9~23.0	6.29	0.681	11.38	0.582	10.808
22.8~22.9	6.26	0.680	11.37	0.580	10.793
22.7~22.8	6.24	0.679	11.37	0.579	10.777
22.6~22.7	6.21	0.678	11.36	0.577	10.761
22.5~22.6	6.18	0.677	11.35	0.575	10.745
22.4~22.5	6.15	0.677	11.35	0.574	10.729
22.3~22.4	6.13	0.676	11.34	0.572	10.713
22.2~22.3	6.10	0.675	11.33	0.570	10.697
22.1~22.2	6.07	0.674	11.33	0.569	10.680
22.0~22.1	6.05	0.673	11.32	0.567	10.668
21.9~22.0	6.02	0.672	11.31	0.565	10.645
21.8~21.9	5.99	0.672	11.31	0.563	10.631
21.7~21.8	5.96	0.671	11.30	0.562	10.616

Measured at Standard Testing Conditions(STC):1000W/m²,25°C,AM1.5G
Ed.2(2008)Allfigures are averages with $\pm 1.5\%$ tolerance.

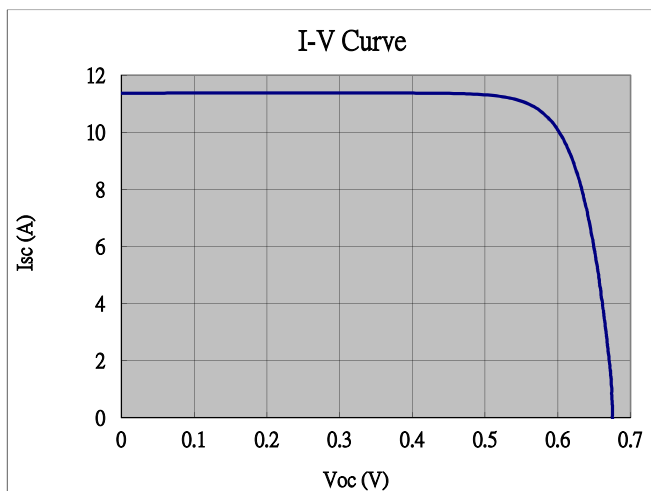
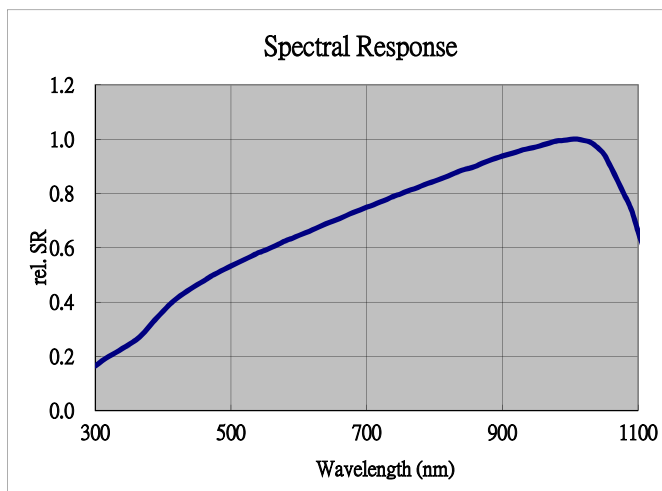
3 Mechanical Design

Square length	166.00 $\pm$ 0.5mm
Diameter	223 $\pm$ 0.5mm
Thickness	170 $\pm$ 30um
Front surface	1 mm silver pad, blue anti-reflecting silicon nitride coating
Back surface	1.4 mm silver wide soldering pads,aluminum surface field
Base material	p-type Mono-crystalline silicon wafer
Junction	Phosphorous diffused N on P

4 TEMPERATURE COEFFICIENTS

Voltage	-0.2806 %/K
Current	0.0449 %/K
Power	-0.367 %/K

5 ELECTRICAL CURVES



Calibration by Fraunhofer ISE Freiburg,under STC. Specifications subject to technical changes.

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